

# DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING


## Jalpaiguri Government Engineering College

### NOTICE

Extra classes for advanced level problem solving are arranged for 3rd year students as per the following time table:

Routine for 3rd year		
Date		Subject
11/03/2024	Time: 5:00PM-6:00PM	Digital Electronics
12/03/2024	Time: 5:00PM-6:00PM	Control System
13/03/2024	Time: 5:00PM-6:00PM	Network Theory
14/03/2024	Time: 5:00PM-6:00PM	Communication Engineering
15/03/2024	Time: 5:00PM-6:00PM	Digital Electronics
16/03/2024	Time: 2:00PM-3:00PM	Control System
18/03/2024	Time: 5:00PM-6:00PM	Digital Electronics
19/03/2024	Time: 5:00PM-6:00PM	Control System
20/03/2024	Time: 5:00PM-6:00PM	Network Theory
21/03/2024	Time: 5:00PM-6:00PM	Communication Engineering
22/03/2024	Time: 5:00PM-6:00PM	Network Theory
23/03/2024	Time: 2:00PM-3:00PM	Communication Engineering
26/03/2024	Time: 5:00PM-6:00PM	Digital Electronics
27/03/2024	Time: 5:00PM-6:00PM	Control System
28/03/2024	Time: 5:00PM-6:00PM	Network Theory
30/03/2024	Time: 2:00PM-3:00PM	Communication Engineering
02/04/2024	Time: 5:00PM-6:00PM	Digital Electronics
03/04/2024	Time: 5:00PM-6:00PM	Control System

Interested students are instructed to attend the classes.

  
5/3/2024  
Head, ECE  
Professor & Head  
Department of Electronics & Communication Engg.  
Jalpaiguri Govt. Engg. College  
Jalpaiguri-735102, W.B., India

# DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING


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Remedial classes for 2<sup>nd</sup> year and 3<sup>rd</sup> year are arranged as per the routine bellow:

Routine for 2 <sup>nd</sup> year		
Date	Time	Subject
28/11/2023	Time: 5:00PM-6:00PM	Analog Electronics
29/11/2023	Time: 5:00PM-6:00PM	EM Theory and antenna
30/11/2023	Time: 5:00PM-6:00PM	Digital Electronics
01/12/2023	Time: 5:00PM-6:00PM	Design and Analysis of Algorithm
02/12/2023	Time: 2:00PM-3:00PM	Analog Electronics
4/12/2023	Time: 5:00PM-6:00PM	EM Theory and antenna
5/12/2023	Time: 5:00PM-6:00PM	Digital Electronics
6/12/2023	Time: 5:00PM-6:00PM	Design and Analysis of Algorithm
7/12/2023	Time: 5:00PM-6:00PM	Analog Electronics
8/12/2023	Time: 5:00PM-6:00PM	EM Theory and antenna
9/12/2023	Time: 2:00PM-3:00PM	Digital Electronics
11/12/2023	Time: 5:00PM-6:00PM	Design and Analysis of Algorithm
12/12/2023	Time: 5:00PM-6:00PM	Analog Electronics
13/12/2023	Time: 5:00PM-6:00PM	EM Theory and antenna
14/12/2023	Time: 5:00PM-6:00PM	Digital Electronics
15/12/2023	Time: 5:00PM-6:00PM	Design and Analysis of Algorithm
16/12/2023	Time: 2:00PM-3:00PM	EM Theory and Antenna
Routine for 3 <sup>rd</sup> year		
Date	Time	Subject
28/11/2023	Time: 5:00PM-6:00PM	VLSI
29/11/2023	Time: 5:00PM-6:00PM	DSP
30/11/2023	Time: 5:00PM-6:00PM	Digital Communication
01/12/2023	Time: 5:00PM-6:00PM	Embedded System
02/12/2023	Time: 2:00PM-3:00PM	VLSI
4/12/2023	Time: 5:00PM-6:00PM	VLSI
5/12/2023	Time: 5:00PM-6:00PM	DSP
6/12/2023	Time: 5:00PM-6:00PM	Digital Communication
7/12/2023	Time: 5:00PM-6:00PM	Embedded System
8/12/2023	Time: 5:00PM-6:00PM	VLSI
9/12/2023	Time: 2:00PM-3:00PM	DSP
11/12/2023	Time: 5:00PM-6:00PM	VLSI
12/12/2023	Time: 5:00PM-6:00PM	DSP
13/12/2023	Time: 5:00PM-6:00PM	Digital Communication
14/12/2023	Time: 5:00PM-6:00PM	Embedded System
15/12/2023	Time: 5:00PM-6:00PM	DSP
16/12/2023	Time: 2:00PM-3:00PM	Digital communication

Interested students are instructed to attend the remedial classes.

  
 23/11/2023  
 Head, ECE  
 Professor & Head  
 Department of Electronics & Communication Engg.  
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 Jalpaiguri-735102, W.B., India