

# Jalpaiguri Government Engineering College

(An Autonomous & NAAC Accredited Institute)
Jalpaiguri-735102, West Bengal, India

website: www.jgec.ac.in

# **Electrical Engineering Department**

Date: 10-10-2024

# Action Taken Report Slow and Advance Learners of BTech (EE) & MTech (EE) students Session 2023-24

Electrical Engineering Department as usual, continues to follow below mentioned steps/plans in order to ensure quality teaching and learning practices:

### A. Need of Monitoring Slow and Advance Learners:

Throughout the process of teaching and learning, teachers have to deal with students with varying learning capabilities. In this college or department, students get admission through WBJEE and WBJELET. Still, it has been noticed that some students are exceptionally intelligent and bright who understand, learn very fast and some are not as strong and some are quite weak to learn and grasp the instructions very slowly. Some students just need direction; some require hard work along with regular guidance and mentoring. In order to assess a student's progress, teachers need to take into account their individual learning attitudes and habits. Some prudent steps such as, advanced learners to be inspired to deliver better than their current performances, particularly when they are aspiring for higher studies and the slow learners to be motivated to pay more interest and effort to achieve better outcome.

### B. The department selects/identifies the slow and advanced learner on the basis of following criteria:

1)Class Participation, 2) Performance in Class tests, Assignment / and Sessional examination, 3)Performance/responses in the Class, 4) Performance in the previous examination.

(if, obtained marks / performance is less than 40%, then identify him/her as slow learner or if, it is more than 80%, then identify as advanced learners)

### C. Activities to be Conducted for Slow learners:

### 1) Remedial Classes

The EE department regularly organizes following remedial classes for 1-2 hours after normal class hours (after 5:00 pm) for slow learners of EE department. Specially, the 3<sup>rd</sup> semester lateral entry students who get late admission in the BTech program, needs special classes to make up their lag. Accordingly, along with remedial classes for BTech(EE) and MTech(EE)- students on the basis of the needs of the slow learner students as identified by the concerned teachers, during odd semesters, remedial classes on the following 3 departmental papers/courses (With PC codes) for the 3<sup>rd</sup> semester later entry students will be conducted on regular basis as per as practicable by the concerned class teachers. This class does not create persistent barriers for regular classes. Progress of the slow learners will be regularly observed and assessed by the concerned teachers.

(i) PC-EE 303 Electrical Circuit Analysis (3-0-0-3-3) - Teacher (TKM)
(ii) PC-EE 301 Analog Electronic Circuits (3-0-0-3-3) -Teacher (SG)
(iii) PC-EE 302 Electrical Machines-I (3-0-0-3-3) -Teacher (GKP)

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### 2) Special Classes / Lectures/Seminars

The department with a specific topic or purpose organizes special classes. Sometimes it is organized around a common theme with advanced and slower students

### D. Conduction of activities for advanced learners:

- Provided Special classes (on Electrical Machines, Circuit Theory, EM-Fields, Power Systems, Power Electronics & Control Systems) for advanced learners on some advanced topics helpful to crack exams like GATE and other.
- 2) Advanced assignments or tasks like participation in Seminars /Conferences/ Technical events
- 3) Contribution in questionnaire preparation and conduction of case studies and support in team building activities

### E. Expected Outcomes:

- 1) Ensure the enhancement of subject knowledge of both advance and slow learners.
- 2) Increasing performance of advanced learners in GATE, any other competitive examinations and also in campus placement drives.
- 3) Improve the presentations and communications skills of advanced learners.
- 4) Develop confidence level among the slow learners.
- 5) Ensure regular and continuous involvement of slow learner in the teaching-learning process.
- 6) Improvements in university exam results of identified slow learners.
- 7) Keep the Students self motivated.
- 8) Develop and enhance interest in the chosen subjects of both slow and advanced learners.

#### F. Observations:

The faculties of the EE department, regularly observes the progress of the students. It is to observe that the performance in marks increased significantly, especially in slow learners. It further shows that test result has reduced the mark difference between slow and advanced students. It is further believed that the system is still in its infancy in our department and it will evolve and improve over time so that students can move towards a better future. Instances of a few such positive outcomes are presented below:

- 1) Soham Sen, Kaustav Narjeenary, Uttaran Nath Sarkar and Arka Biswas *qualified GATE 2024* and 3 of them went for higher study.
- 2) Soham Sen joined the program: Erasmus Mundus Joint Masters in Game Design (REPLAY), Universities: Lusofóna University (1st Semester), LUCA School of Arts (2nd Semester), Aalto University (3rd semester), Current location: Lisbon, Portugal in 2024.
- 3) Soham Sen, **BTech(EE) Batch of 2020-2024:** MITACS Globalink Research Intern 2023, Dalhousie University, Nova Scotia, Canada. Supervisor: Prof Rita Orji. Topic: 'Games and Gamification to Promote Health and Wellness'. He got selected for one of **Canada's and World's largest Hackathons, Hack the North** held at **University of Waterloo** from 15-17 September, 2023.
- 4) Soham Bhattacharyya, BTech(EE) Batch of 2020-2024: UNMESH 2023 under CESC Ltd. from 22 May ,2023 to 15th July, 2023 for 8 weeks. It was a project making competition where he was assigned under Construction department and the project given to him was "Developing People Counting System installed at Building Entrances for Emergency Evacuation Purposes at Prinsep Street Office", He was awarded 1st runner up position

Professor & H.Head, EE Department

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